

40V, 120A
N-channel Enhancement Mode Power MOSFET
 $R_{DS(ON)} < 4.3m\Omega @ V_{GS}=10V$
 $R_{DS(ON)} < 7.5m\Omega @ V_{GS}=4.5V$

Lead free and Green Device Available

 Excellent $R_{DS(ON)}$ and Low Gate Charge

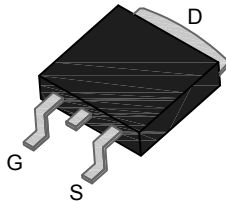
Lead free product is acquired


Product Summary

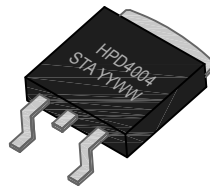
V_{DS}	40	V
$R_{DS(ON)}$ Max.	4.3	m Ω
I_D	120	A

Application:

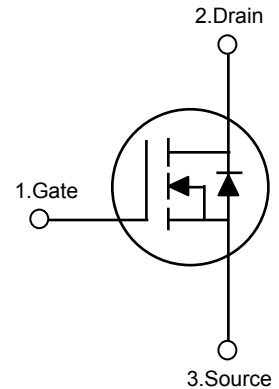
Load Switch、PWM Application、Power management

 100% UIS TESTED! 100% ΔV_{DS} TESTED!


TO-252(DPAK) top view



Marking and pin Assignment


ORDERING INFORMATION

Order Number	Package	Pin Assignment			Packing	Reel Size	Reel	Per Carton
		1	2	3			Pcs	Pcs
HPD4004STA	TO-252	G	D	S	Tape Reel	13 Inch	2500	25000

Absolute Maximum Ratings ($T_C=25^\circ C$ unless otherwise specified)

Symbol	Parameter	Max.	Units
V_{DSS}	Drain-Source Voltage	40	V
V_{GSS}	Gate-Source Voltage	± 20	
I_D	Continuous Drain Current	$T_C=25^\circ C$	120
		$T_C=100^\circ C$	78
I_{DM}	Pulsed Drain Current ^{note1}	480	A
E_{AS}	Single Pulsed Avalanche Energy ^{note2}	160	mJ
P_D	Power Dissipation	$T_C=25^\circ C$	108
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.4	$^\circ C/W$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +175	$^\circ C$

Electrical Characteristics ($T_C=25^\circ\text{C}$, unless otherwise specified)**Off Characteristic**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	40	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=40V, V_{GS}=0V$	--	--	1.0	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0V, V_{GS}=\pm 20V$	--	--	± 100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1	1.6	2.5	V
$R_{DS(on)}$	Static Drain-Source on-Resistance (note3)	$V_{GS}=10V, I_D=30A$	--	3.3	4.3	m Ω
		$V_{GS}=4.5V, I_D=20A$	--	5.4	7.5	

Dynamic Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
C_{iss}	Input Capacitance	$V_{DS}=20V$ $V_{GS}=0V$ $f=1.0MHz$	--	5595	--	pF
C_{oss}	Output Capacitance		--	411	--	
C_{riss}	Reverse Transfer Capacitance		--	340	--	
Q_g	Total Gate Charge	$V_{DS}=20V$ $I_D=30A$ $V_{GS}=10V$	--	65	--	nC
Q_{gs}	Gate-Source Charge		--	12.5	--	
Q_{gd}	Gate-Drain("Miller") Charge		--	15	--	

Electrical Characteristics ($T_C=25^\circ\text{C}$, unless otherwise specified)**Switching Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=20V$ $I_D=30A$ $R_L=1\Omega$ $R_{GEN}=3\Omega$ $V_{GS}=10V$	--	12	--	nS
t_r	Turn-on Rise Time		--	16	--	
$t_{d(off)}$	Turn-off Delay Time		--	39	--	
t_f	Turn-off Fall Time		--	15	--	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Maximum Continuous Drain to Source Diode Forward Current		--	--	120	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		--	--	480	
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_S=30A$	--	--	1.2	V
t_{rr}	Body Diode Reverse Recovery Time	$T_J=25^\circ\text{C}$ $I_F=30A$ $dI/dt=100A/\mu\text{s}$	--	22	--	nS
Q_{rr}	Body Diode Reverse Recovery Charge		--	11	--	nC

Notes:

- 1、Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature
- 2、EAS condition : $T_J=25^\circ\text{C}$, $V_{DD}=20V$, $V_G=10V$, $R_G=25\Omega$, $L=0.5mH$, $I_{AS}=25.3A$
- 3、Pulse Test : Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 0.5\%$



Typical Performance Characteristics

FIG-1: Output Characteristics

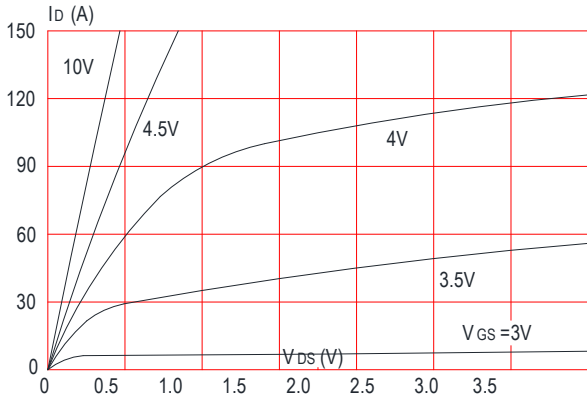


FIG-2: Typical Transfer Characteristics

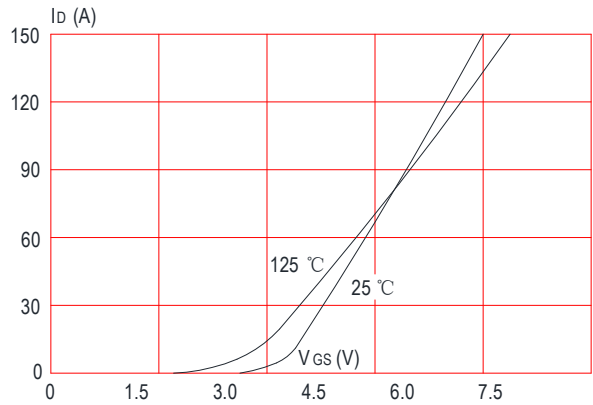


FIG-3: On-resistance vs. Drain Current

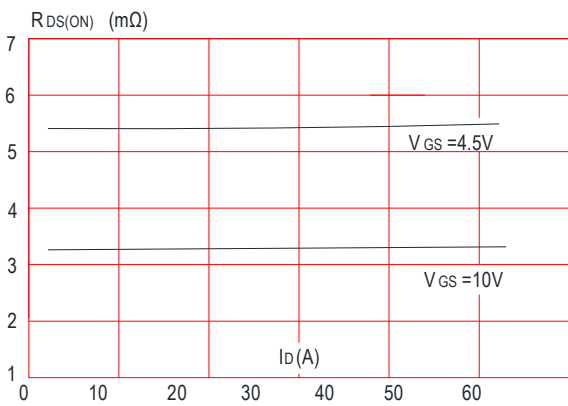


FIG-4: Body Diode Characteristics

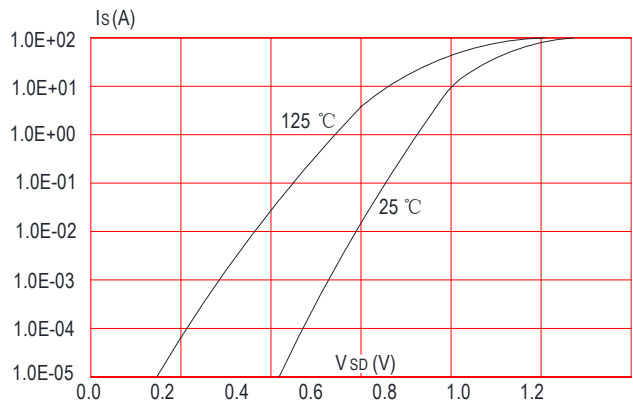


FIG-5: Gate Charge Characteristics

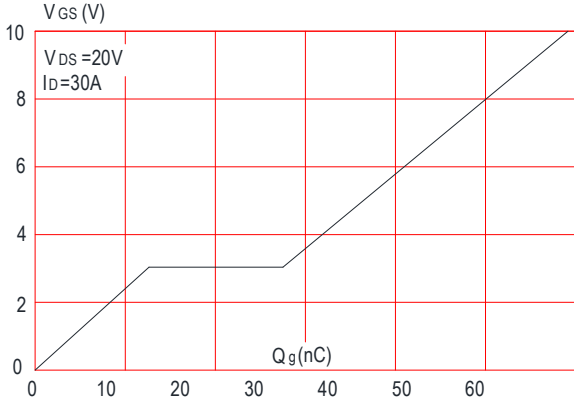


FIG-6: Capacitance Characteristics

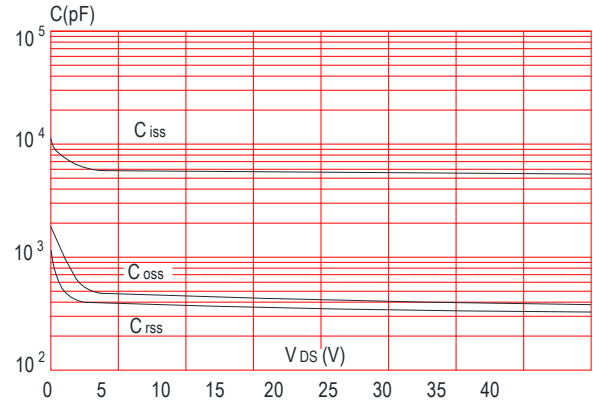


FIG-7: Normalized Breakdown Voltage vs. Junction Temperature

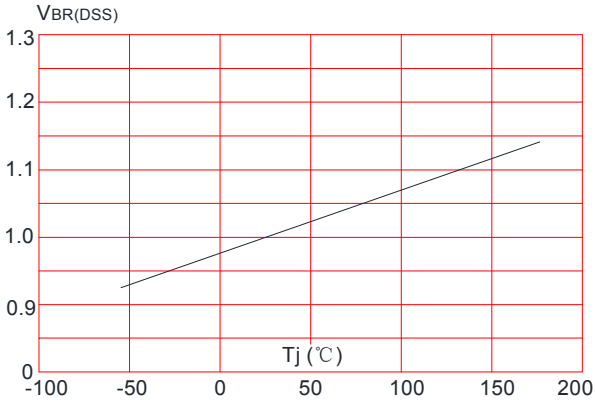


FIG-8: Normalized on Resistance vs. Junction Temperature

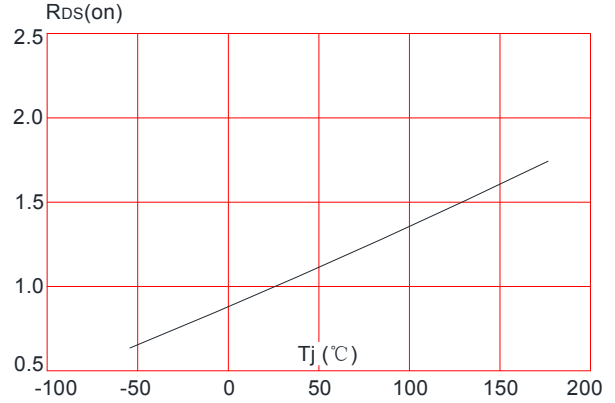


FIG-9: Maximum Safe Operating Area

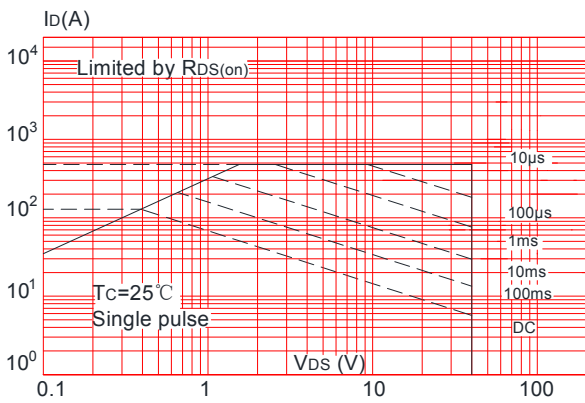


FIG-10: Maximum Continuous Drain Current vs. Case Temperature

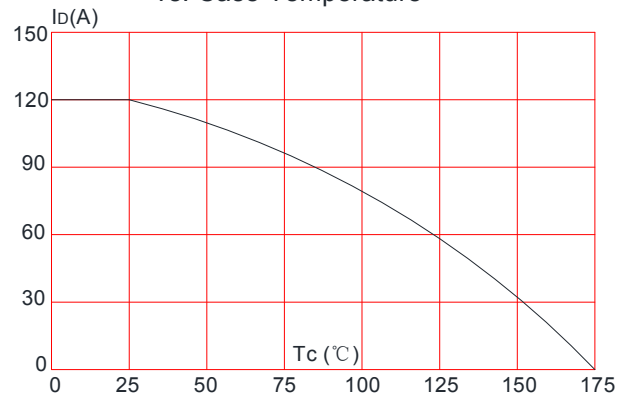
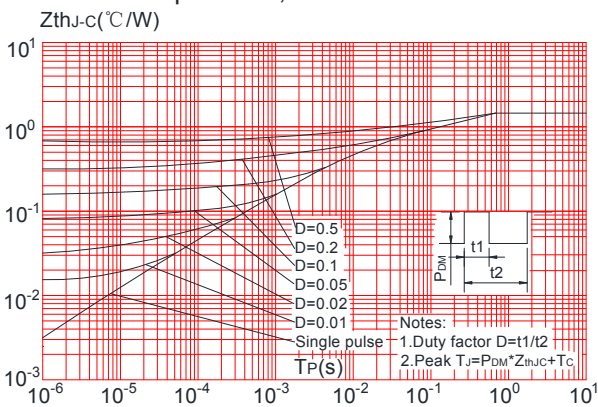


FIG-11: Maximum Effective Transient Thermal Impedance, Junction-to-Case





Test Circuit

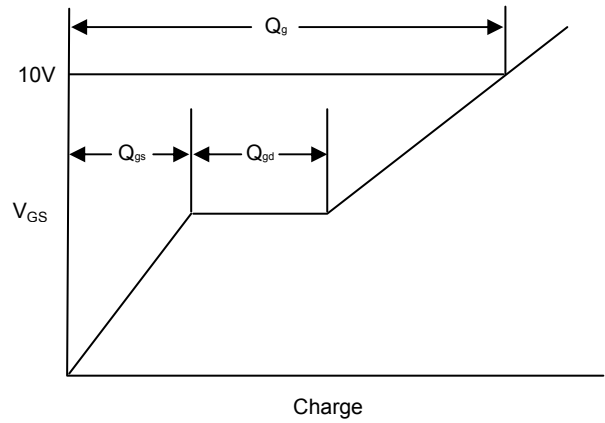
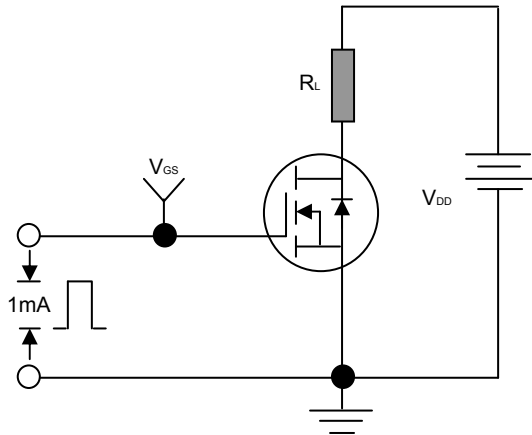


Fig-1: Gate Charge Test Circuit & Waveform

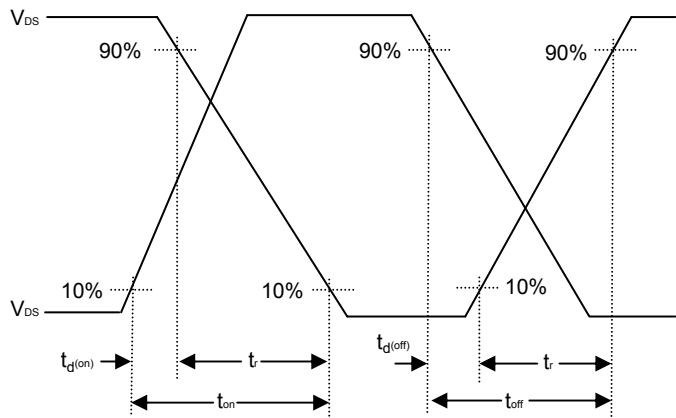
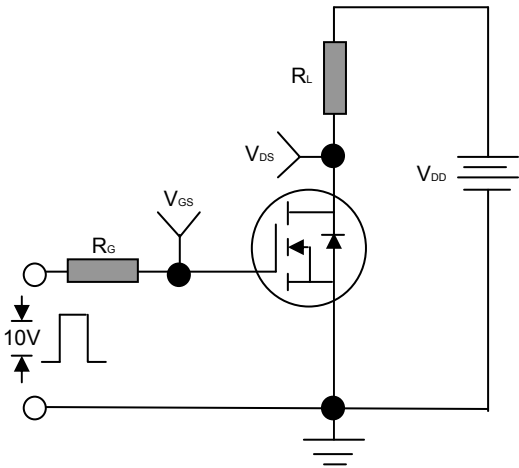


Fig-2: Resistive Switching Test Circuit & Waveforms

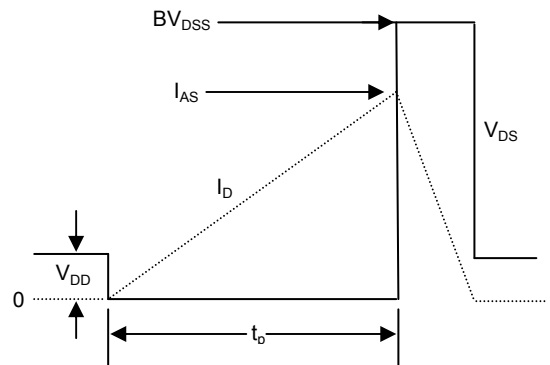
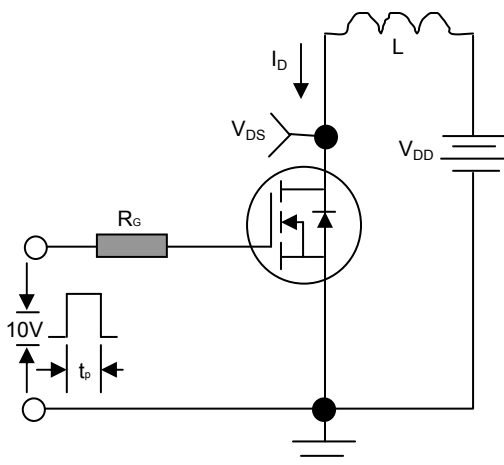
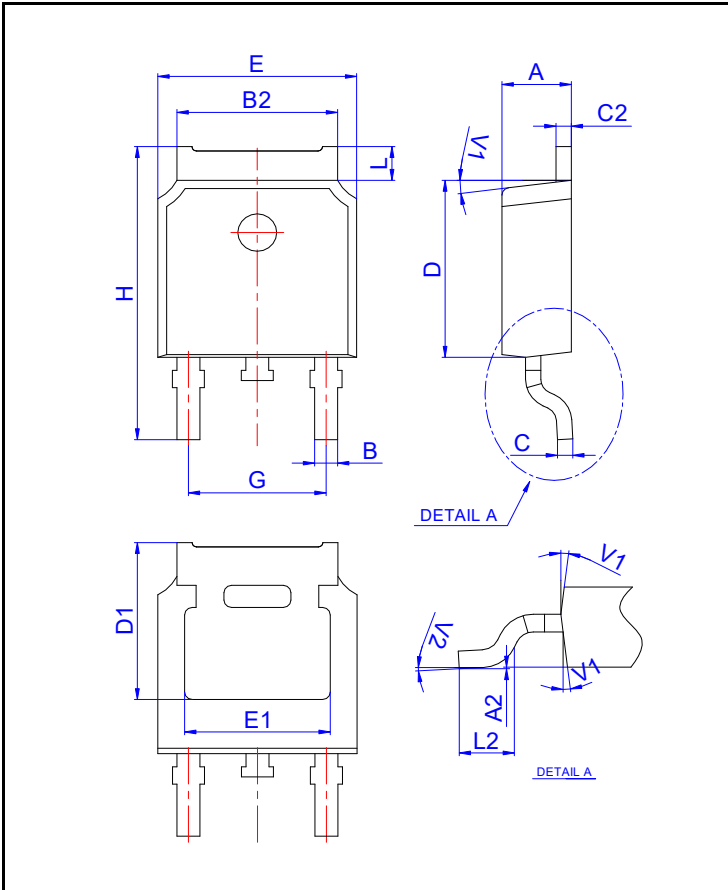


Fig-3: Unclamped Inductive Switching Test Circuit & Waveforms

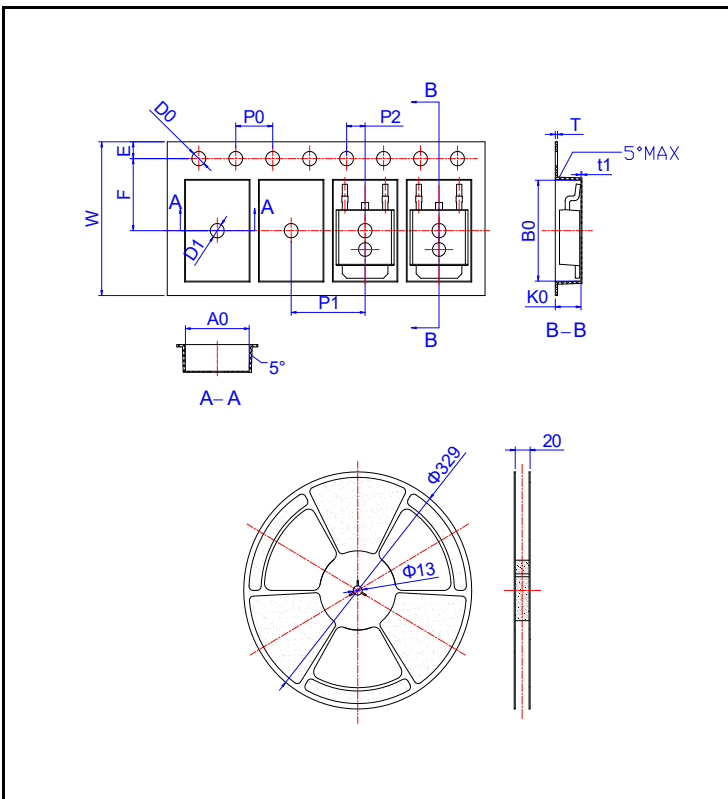


TO-252 (DPAK) PACKAGE MECHANICAL DATA (mm & inch)



REF.	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	2.10	2.50	0.083	0.098
A2	0	0.10	0	0.004
B	0.66	0.86	0.026	0.034
B2	5.18	5.48	0.202	0.216
C	0.40	0.60	0.016	0.024
C2	0.44	0.58	0.017	0.023
D	5.90	6.30	0.232	0.248
D1	5.30 REF		0.209 REF	
E	6.40	6.80	0.252	0.268
E1	4.63		0.182	
G	4.47	4.67	0.176	0.184
H	9.50	10.7	0.374	0.421
L	1.09	1.21	0.043	0.048
L2	1.35	1.65	0.053	0.065
V1	7° TYP.		7° TYP.	
V2	0°	6°	0°	6°

TO-252 Reel Specification



REF.	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
W	15.90	16.10	0.626	0.634
E	1.65	1.85	0.065	0.073
F	7.40	7.60	0.291	0.299
D0	1.40	1.60	0.055	0.063
D1	1.40	1.60	0.055	0.063
P0	3.90	4.10	0.154	0.161
P1	7.90	8.10	0.311	0.319
P2	1.90	2.10	0.075	0.083
A0	6.85	7.00	0.270	0.276
B0	10.45	10.60	0.411	0.417
K0	2.63	2.88	0.105	0.113
T	0.24	0.27	0.009	0.011
t1	0.10		0.004	
10P0	39.80	40.20	1.567	1.583



经中华人民共和国工商行政管理总局商标局批准

HHE 图案、字母、均为我公司正式注册商标，仿冒、盗用均属侵权，违法必究！

WARN, Letters, patterns, are officially registered my trademark counterfeiting, theft are all violations, violators will be held liable !

深圳市浩海电子有限公司

SHENZHEN HAOHAI ELECTRONICS CO., LTD.

2 floor(whole floor), BAOXIN Building, 0 Lane on the 8th, Yufeng Garden,
82 District, BAOAN District, Shenzhen City, Guangdong Province, China.

公司电话 TEL: +86-755-29955080、29955081、29955082、29955083

FAX: +86-755-27801767

E-mail: kkg@kkg.com.cn

<http://www.szhhe.com>

<http://www.kkg.com.cn>