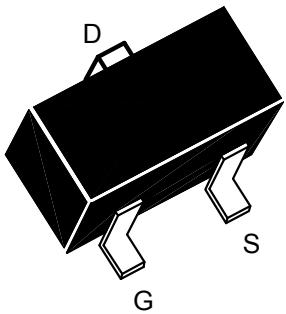
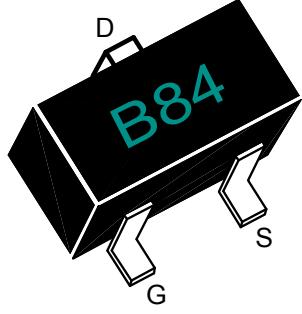
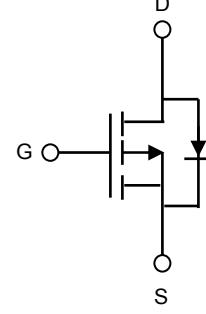




## P-channel Enhancement Mode Power MOSFET

<b>Features</b>	<b>Application</b>
<ul style="list-style-type: none"> <li>◆ <math>V_{DS}=-50V</math>, <math>I_D=-0.13A</math></li> <li>◆ <math>R_{DS(ON)}&lt;3.6m\Omega @ V_{GS}=-10V</math></li> <li>◆ <math>R_{DS(ON)}&lt;5.4m\Omega @ V_{GS}=-4.5V</math></li> <li>◆ Advanced Trench Technology</li> <li>◆ Provide Excellent <math>R_{DS(ON)}</math> and Low Gate Charge</li> <li>◆ Lead free product is acquired</li> </ul>	 <ul style="list-style-type: none"> <li>◆ Load Switch</li> <li>◆ PWM Application</li> <li>◆ Power Management</li> </ul> <p>100% UIS TESTED! 100% <math>\Delta V_{ds}</math> TESTED!</p>

Marking and pin Assignment	SOT-23 top view	Schematic Diagram
		

Absolute Maximum Ratings ( $T_c=25^\circ C$  unless otherwise specified)

Symbol	Parameter	Max.	Unit
$V_{DSS}$	Drain-Source Voltage	-50	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	
$I_D$	Continuous Drain Current	$T_A=25^\circ C$	A
		$T_A=100^\circ C$	
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>	-0.52	
$P_D$	Power Dissipation	$T_A=25^\circ C$	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	556	°C/W
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55~+150	°C

Electrical Characteristics ( $T_J=25^\circ\text{C}$  unless otherwise specified)

## Off Characteristic

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	-50	--	--	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS}=-50\text{V}$ , $V_{GS}=0\text{V}$	--	--	-1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}=\pm 20\text{V}$	--	--	$\pm 100$	nA

## On Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D=250\mu\text{A}$	--	-1.5	-2.5	V
$R_{DS(\text{on})}$	Static Drain-Source on-Resistance <sup>note3</sup>	$V_{GS}=10\text{V}$ , $I_D=-0.13\text{A}$	--	2.2	3.6	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$ , $I_D=0.1\text{A}$	--	2.6	5.4	

## Dynamic Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
$C_{iss}$	Input Capacitance	$V_{DS}=-25\text{V}$ $V_{GS}=0\text{V}$ $f=1.0\text{MHz}$	--	30	--	$\text{pF}$
$C_{oss}$	Output Capacitance		--	10	--	
$C_{rss}$	Reverse Transfer Capacitance		--	5	--	
$Q_g$	Total Gate Charge	$V_{DS}=-25\text{V}$ $I_D=-0.13\text{A}$ $V_{GS}=10\text{V}$	--	4.5	--	$\text{nC}$
$Q_{gs}$	Gate-Source Charge		--	0.8	--	
$Q_{gd}$	Gate-Drain("Miller") Charge		--	1.2	--	

## Switching Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
$t_{d(on)}$	Turn-on Delay Time	$V_{DS}=-2.5\text{V}$ $I_D=-0.1\text{A}$ $R_{\text{GEN}}=2.5\Omega$ $V_{GS}=-10\text{V}$	--	2.5	--	$\text{nS}$
$t_r$	Turn-on Rise Time		--	1	--	
$t_{d(off)}$	Turn-off Delay Time		--	16	--	
$t_f$	Turn-off Fall Time		--	8	--	

## Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units	
$I_s$	Maximum Continuous Drain to Source Diode Forward Current	$V_{GS}=0\text{V}$ , $I_s=-0.13\text{A}$	--	--	-0.13	$\text{A}$	
$I_{SM}$			--	--	-0.52		
$V_{SD}$	Drain to Source Diode Forward Voltage		$V_{GS}=0\text{V}$ , $I_s=-0.13\text{A}$	--	-0.8	-1.2	V

## Notes:

1 . Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature

3 . Pulse Test : Pulse Width  $\leqslant 300\mu\text{s}$ , Duty Cycle  $\leqslant 2\%$

## Typical Performance Characteristics

FIG-1: Output Characteristics

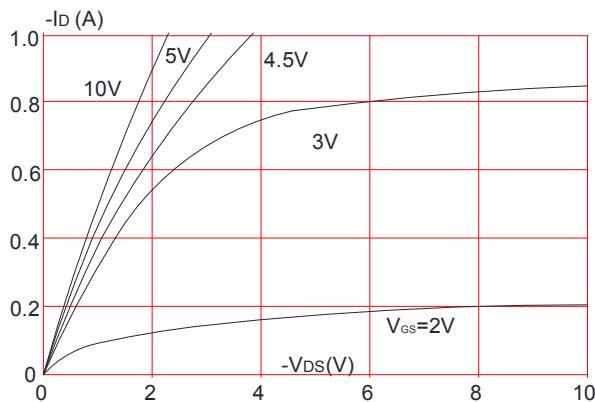


FIG-2: Typical Transfer Characteristics

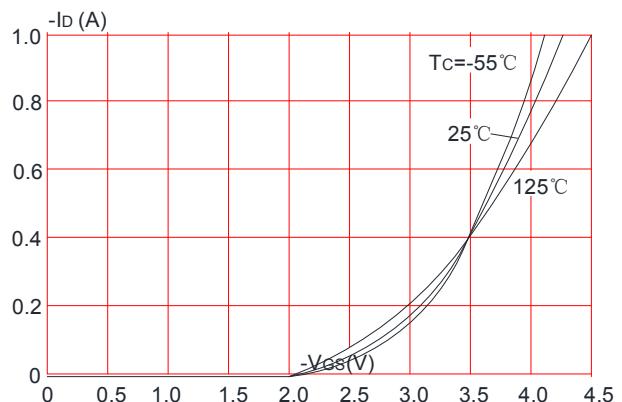


FIG-3: On-resistance vs. Drain Current

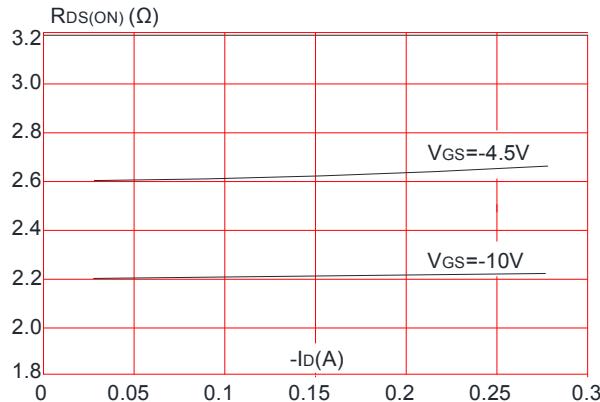


FIG-4: Body Diode Characteristics

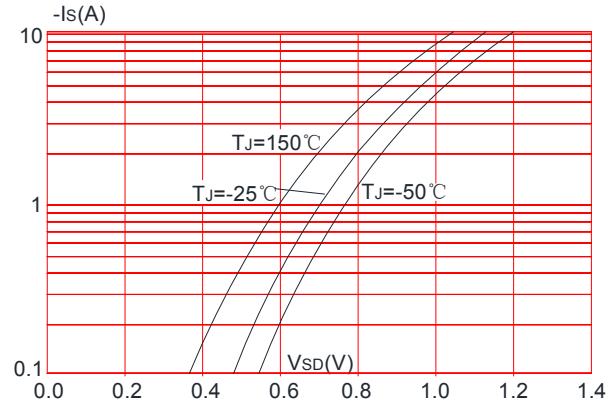


FIG-5: Gate Charge Characteristics

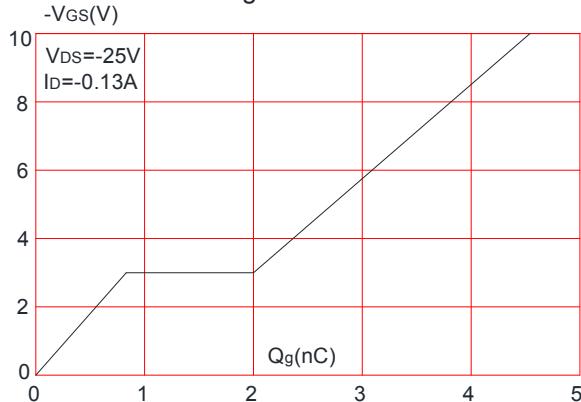


FIG-6: Capacitance Characteristics

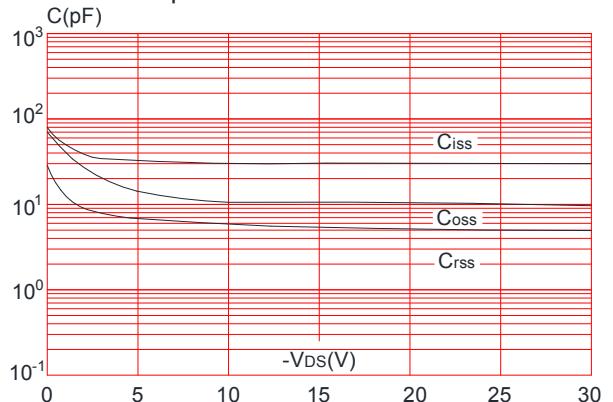




FIG-7: Normalized Breakdown Voltage vs. Junction Temperature

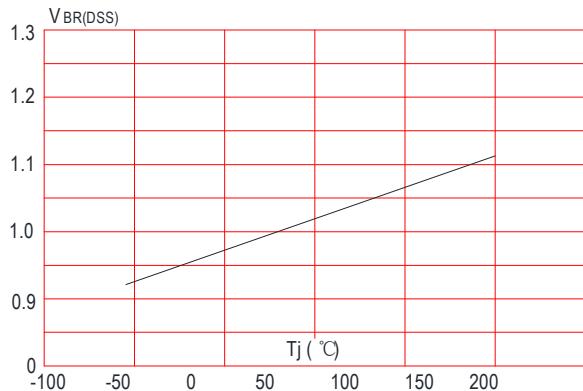


FIG-8: Normalized on Resistance vs. Junction Temperature

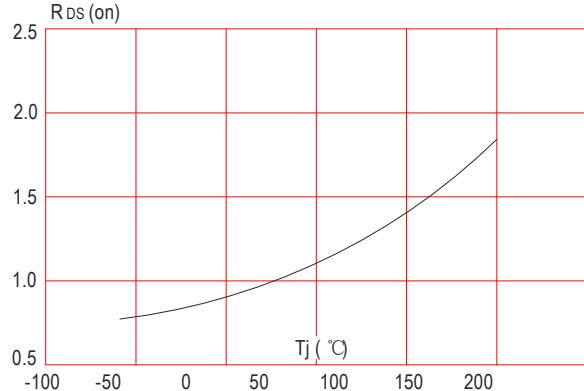


FIG-9: Maximum Safe Operating Area

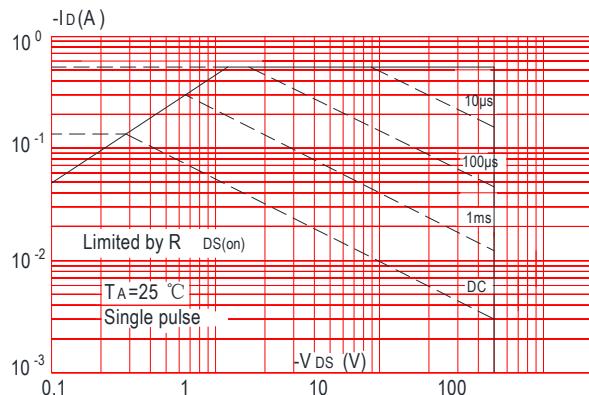


FIG-10: Maximum Continuous Drain Current vs. Ambient Temperature

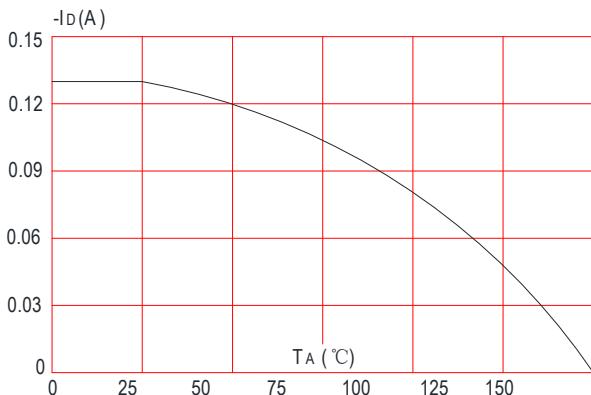
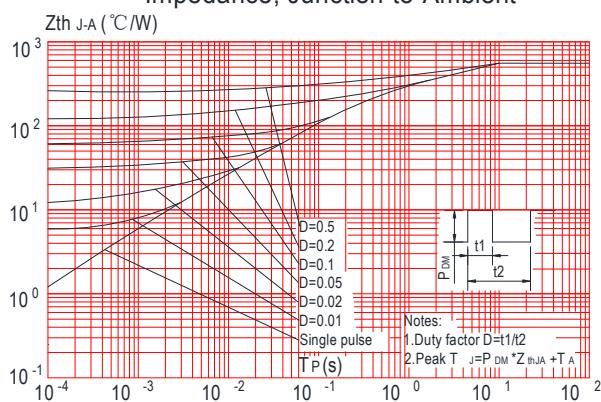


FIG-11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



## TEST CIRCUITS AND WAVEFORMS

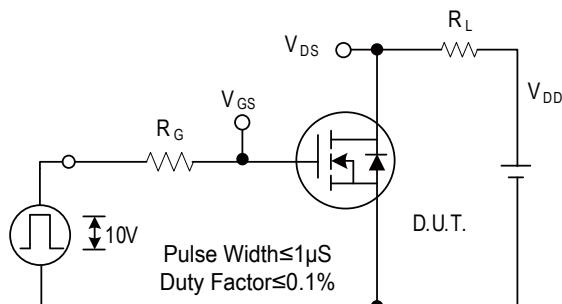


Fig-2A: Switching Test Circuit

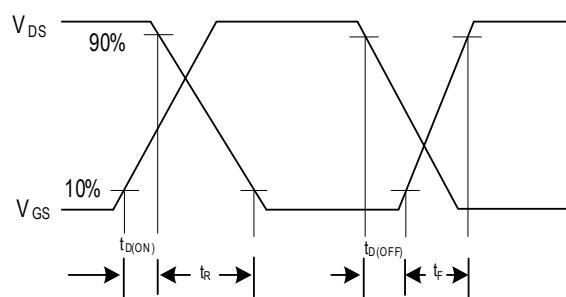


Fig-2B: Switching Waveforms

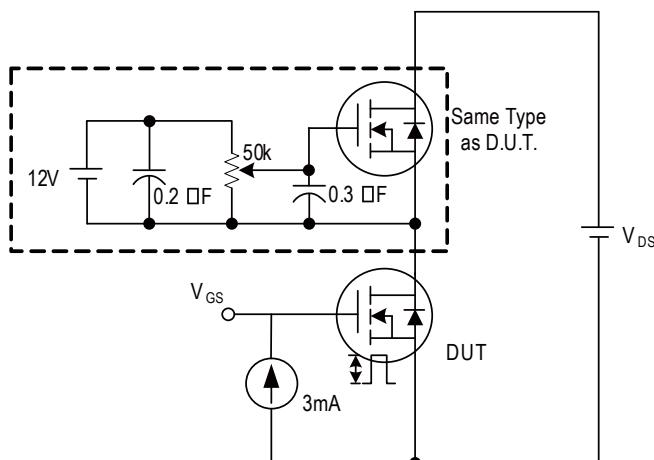


Fig-3A: Gate Charge Test Circuit

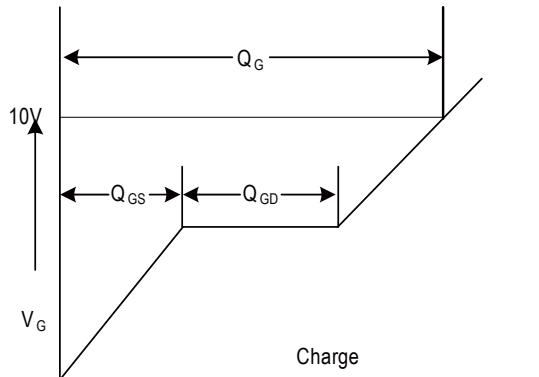


Fig-3B: Gate Charge Waveform

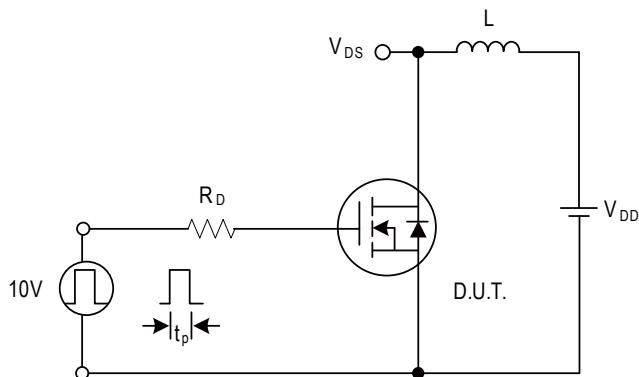


Fig-4A: Unclamped Inductive Switching Test Circuit

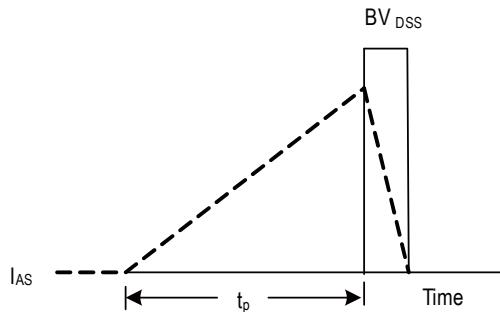


Fig-4B: Unclamped Inductive Switching Waveforms



## SOT-23 PACKAGE MECHANICAL DATA

Ret.	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	2.30	2.50	0.091	0.098
B	2.80	3.00	0.110	0.118
C	1.90 REF		0.075 REF	
D	0.35	0.45	0.014	0.018
E	1.20	1.40	0.047	0.055
F	0.90	1.10	0.035	0.043
G		0.15		0.008
H	0.20		0.008	
I	0	0.10	0	0.004

## Package Information-SOT-23

Ret.	Dimensions	
	Millimeters	Inches
A0	3.15 ±0.3	0.124 ±0.012
B0	2.77 ±0.3	0.109 ±0.012
C	178	7
D0	1.50 ±0.1	0.059 ±0.004
E	1.75 ±0.2	0.069 ±0.008
E1	13.3 ±0.3	0.524 ±0.012
F	3.50 ±0.2	0.138 ±0.008
P0	4.00 ±0.2	0.157 ±0.008
P1	4.00 ±0.2	0.157 ±0.008
P2	2.00 ±0.2	0.079 ±0.008
W	8.00 ±0.2	0.315 ±0.008
W1	11.5 ±1.0	0.453 ±0.039

## SOT-23 PACKAGE INFORMATION

Device model	Marking	OUTLINE	Device Package	Reel Size	Reel (Pcs)	Per Carton (Pcs)
HPMB84A	B84	TAPING	SOT-23	7 Inch	3000 (3K)	180000 (180K)



Manufacturers version information

2020-03-10 , HAOHAU™ Product Data-1.0

2021-09-17 , HAOHAU™ Product Data-1.1



经中华人民共和国工商行政管理总局商标局批准

HAOHAU、HHE 图案、字母、均为我公司正式注册商标，仿冒、盗用均属侵权，违法必究！

WARN, Letters, patterns, are officially registered my trademark counterfeiting, theft are all violations, violators will be held liable !

**深圳市浩海電子有限公司**  
**SHENZHEN HAOHAI ELECTRONICS CO., LTD.**

2 floor(whole floor), BAOXIN Building, 0 Lane on the 8th. Yufeng Garden.  
82 District. BAOAN District, Shenzhen City, Guangdong Province, China.

TEL: +86-755-29955080、29955081、29955082、29955083

FAX: +86-755-27801767  
<http://www.szhhe.com>

E-mail: [kkg@kkg.com.cn](mailto:kkg@kkg.com.cn)  
<http://www.kkg.com.cn>